Attorney's Docket No.: MP0237/13361/049001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Sehat Sutardja Art Unit: Unknown Serial No.: 10/621,058 Examiner: Unknown

Filed : July 15, 2003

Title : LOW LOSS DC/DC CONVERTER

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information
Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

Each enclosed document listed on the herewith Form PTO-1449 was cited in the attached European Search Report dated December 1, 2004, issued in a foreign counterpart application not more than three months ago. To the best knowledge of the undersigned, this is the first citation of these items in any communication from a foreign patent office in a counterpart foreign application.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 01/26/05

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Sheet	1	of	1

Substitute Form PTO-1449

U.S. Department of Commerce Patent and Trademark Office

MP0237/13361/049001

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Application No. 10/621,058

Applicant Sehat Sutardja

Filing Date Group Art Unit

July 15, 2003

STATE OF THE STATE			U.S. Pate	ent Documents			
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,010,261	04/23/1991	Steigerwald			
	AB	5,687,067	11/11/1997	Majid et al.			
	AC						
	AD						
	AE					-	
	AF						
	AG						
	AH						
-	AI						
	AJ						
	AK						

	Foreign Patent Documents or Published Foreign Patent Applications							
Examiner	Desig.	Document	Publication	Country or			Translat	ion
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL	08-182305	07-12-1996	Japan			Abstract Only	
	AM							
	AN							
	AO							
	AP			•				

	Other Documents (include Author, Title, Date, and Place of Publication)				
Examiner Initial	Desig. ID	Document			
	AQ	Scott Deuty, "Optimizing Transistor Performance in Synchronous Rectifier, Buck Converters", IEEE, 2000			
	AR				
	AS				
	AT				

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if no	ot in conformance and not considered. Include copy of this form with